



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Y. OSONE et al

Application No.:

09/943,512

Filed:

August 31, 2001

For:

MULTILAYER WIRING BOARD AND SEMICONDUCTOR

DEVICE

Art Unit:

2827

Examiner:

D. E. GRAYBILL

<u>AMENDMENT</u>

MS: AMENDMENTS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 10, 2006

Sir:

In response to the Office Action dated June 3, 2005, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Specification;

Amendments to the Claims:

Amendments to the Drawings describing changes to the drawings shown in Appendix A; and

Remarks are included following the amendments.

Appendix A including a replacement sheet of drawings; and

Appendix B including a marked-up version of the original Specification corresponding to the Substitute Specification filed Oct. 27, 2004, noting that the attached marked-up version in Appendix B of this Amendment has